



Application Number: 10/707342

2/\$

TITLE OF INVENTION: LOW VOLTAGE ELECTRON SOURCE WITH SELF ALIGNED GATE APERTURES, FABRICATION METHOD THEREOF, AND LUMINOUS DISPLAY USING THE ELECTRON SOURCE

Application Number: 10/707342

Date: February 5, 2004

Commissioner for Patents

Washington DC 20231

Preliminary Amendment

Prior to examination, please amend the above application as following:

1. Specification:

a. Replace paragraph [0033] on page 12 with the following text:

[0033]

The electron source as is described above become addressable when: the first cathode electrode is configured as multiple cathode electrodes each electrically isolated from each other; the gate electrode is configured as multiple gate electrodes, each electrically isolated from each other and intersects with the multiple cathode electrodes; and apertures are formed at the intersection in the gate electrode and the insulator, each exposes and is aligned with one nano-structure from the emitter layer. Activation of a selected cathode and a selected gate electrode will determine one intersection that emits electrons.

02/12/2004 SMINASS1 00000073 10707342

01 FC:2201
02 FC:9998

43.00 OP
4.00 OP